

<b>Notice of References Cited</b>	Application/Control No. 10/666,343		Applicant(s)/Patent Under Reexamination KOTTAPALLI, SAILESH	
	Examiner Aimee J. Li		Art Unit 2183	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,832,260	11-1998	Arora et al.	712/239
*	B	US-6,496,925	12-2002	Rodgers et al.	712/244
*	C	US-6,321,330	11-2001	Doshi et al.	712/241
*	D	US-2002/0112148	08-2002	Wang et al.	712/226
*	E	US-6,442,679	08-2002	Klauser et al.	712/218
*	F	US-2002/0144098	10-2002	Wang et al.	712/221
*	G	US-2004/0193849	09-2004	Dundas, James D.	712/225
*	H	US-6,513,109	01-2003	Gschwind et al.	712/200
*	I	US-6,629,238	09-2003	Arora et al.	712/241
*	J	US-6,662,294	12-2003	Kahle et al.	712/226
*	K	US-6,918,032	07-2005	Abdallah et al.	712/234
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Sprangle, Eric and Patt, Yale. "Facilitating Superscalar Processing via a Combined Static/Dynamic Register Renaming Scheme". ©1994 ACM. Pages 143-147.			
	V	Mahlke et al. "The Program Decision Logic Approach to Predicated Execution". ©1999 IEEE. Pages 208-219.			
	W	Mahlke et al. "A Comparison of Full and Partial Predicated Execution Support for ILP Processors". ©1994 ACM. Pages 138-149.			
	X				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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